A translator of Java programs to TADDs

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Abstract. The model checking tools Uppaal and VerICS accept a description of a network of Timed Automata with Discrete Data (TADDs) as input. Thus, to verify a concurrent program written in Java by means of these tools, first a TADD model of the program must be build. Therefore, we have developed the J2TADD tool that translates a Java program to a network of TADDs; the paper presents this tool.

The J2TADD tool works in two stages. The first one consists in translation of a Java code to an internal assembly language (IAL). Then, the resulting assembly code is translated to a network of TADDs. We exemplify the use of the translator by means of the following well-known concurrency examples written in Java: race condition problem, dining philosophers problem, single sleeping barber problem and readers and writers problem.

1. Introduction

Model checking [3] is a widely recognised and prominent automatic verification technique both in hardware [10] and protocol verification [8]. It does not rely on complicated interaction with the user for incremental property proving. If a property does not hold, the model checker automatically generates a
counterexample. In model checking, the system to be verified is modelled as a finite state machine (for example as a network of timed automata), which further can be represented by a transition system, and temporal logics are used for specifying the system properties.

Typical examples of finite systems, for which model checking has successfully been applied, are digital sequential circuits and communication protocols, and typical examples of checked properties are reachability properties. In this paper, we consider the model checking problem for concurrent programs written in Java, one of the dominant high-level programming languages, and we test these programs for the properties mentioned above.

Verifying programs written in programming languages like Java is different from verifying digital sequential circuits or protocols; the state space is often infinite and the relationships between possible states are harder to understand because of asynchronous behaviour and complex underlying semantics of the languages. Further, the size and complexity of software force us to treat model checking rather as a debugging technique in software verification than a fully automated validation process of the whole software. In particular, for what concerns verification of Java programs, we see model checking as a method that can be applied to the crucial parts of a Java software.

In order to investigate the challenges that Java programs pose for model checking, we have developed a J2TADD tool that translates a Java code to a network of Timed Automata with Discrete Data (TADDs) [9, 14]. J2TADD implements the translation that has been shown in [15] and there one can find a detailed description of how the concepts of concurrent programming in Java are translated into TADDs.

The TADD formalism is accurate enough to detect concurrency errors and yet abstract enough to make model checking tractable. Moreover, it is an input language of the model checking tools VerICS [5] and Uppaal [2], so J2TADD compiler together with these two tools can be used to validate concurrent programs written in Java. J2TADD is a pure Java application that can be run as a standalone command line tool on any platform supporting Java 1.5 or later.

Our case studies have been done for a Java program that suffers from a race condition and for Java programs that implement the following well-known concurrency examples: dining philosophers problem, single sleeping barber problem, and readers and writers problem. As verification engines we have used Uppaal and BMC4TADD - a new version of the BMC module of the tool VerICS that has been adapted to work with the J2TADD translator. BMC4TADD implements the Bounded Model Checking method, where systems are described as a network of TADDs and properties are the reachability one.

Over the last ten years various approaches and tools have been developed to automatically verify Java programs. However, the majority of work has been devoted to analysis of Java bytecode and building new Java Virtual Machines (JVMs) that interpret Java bytecode. In particular, the following two verification and testing environments for Java have been built: Java PathFinder (JPF) [12] and Bandera [4]. Both tools can model check Java programs on deadlocks and limited Java assertions only.

Apart from the two above tools one can find the following works related to the model checking of Java programs [11, 6]. The paper [11] describes a Java model checker that is based on the SAL (Symbolic Analysis Laboratory) [1] intermediate language and the Soot compiler framework. According to the paper the architecture of the tool is the following. First, a Java program is compiled to Java bytecode. Then, the result is converted to a Jimple code, one of the Soot output formalisms. Next, the Jimple code is translated to SAL; this should be done by a Jimple to SAL translator, but it is not available on the web. Thus, it is not possible to run and evaluate the [11] Java model checker. Moreover, it seems that the work is not continued since the year 2000.

The paper [6] presents Java Formal Analysis (JavaFAN), a tool to simulate and formally analyse
multithreaded Java programs at source code and/or bytecode levels. The tool is based on rewriting logic, implemented in the Maude language, and supporting analyses both the Java language and JVM bytecode. It allows for the following types of analysis: symbolic simulation, safety violations (via BFS search), and LTL model checking of rewriting theories. Similarly to the SAL based model checker, JavaFAN is not available on the web. Moreover, it seems that the work is not continued since the year 2006.

The rest of the paper is organised as follows. The next section shortly describes the idea of our translation of a Java code to a network of TADDs. Then, in Section 3 we discuss an assembly language that is used internally by our J2TADD translator. In Section 4 we present two stages of our translation (interpreting and generating transitions) that are required to convert the internal assembly code to TADDs. In Section 5 we show a simple Java program that encodes the dining philosophers problem, the resulting internal assembly code and the final TADD specification resulting from it. Finally, we present a case study that confirms that our approach provides a valuable aid for verification of Java programs.

2. Translation from JAVA to TADD

In this section we shortly describe the idea of our translation of a concurrent multithreaded Java program to a network of Timed Automata with Discrete Data (TADDs) [9, 14]; a detailed description of the translation can be found in [15]. TADDs are standard diagonal timed automata augmented to include integer variables over which standard arithmetic and Boolean expressions can be defined. These automata take as an input a set of initialised integer variables and a set of propositional variables, true at particular states.

Each state of a TADD is an abstraction of a state of a given Java program, and each transition represents an execution of a code transforming this abstract state. The subset of Java that can be translated to a network of TADDs contains: definitions of integer variables, standard programming language constructs like assignments, expressions with most operators, conditional statements and loops (for, while, do while), instructions break and continue without labels, definitions of classes, objects, constructors, static and non–static methods and synchronisation of methods and blocks. Also, standard thread creation constructs and the following special methods: Thread.wait(), Thread.notify(), Thread.join(Thread), Thread.sleep(int) and Random.nextInt(int) are recognised.

A theoretical method of constructing a network of TADDs that models a Java program is shown in [15]. Here we only recall that in TADDs locations are used to record the current control state of each thread and the values of key program variables and any run–time information necessary to implement the concurrent semantics (e.g., whether each thread is ready, running, or blocked on some object). Each transition represents the execution of a Java instruction (for example an assignment statement) for some thread. There is one TADD for each thread, but the main thread: one TADD for each instance of a started thread, and one TADD for each used semaphore. The main thread is treated differently: it is executed by a built–in interpreter instead of being translated like the other threads. The reason for this is twofold:

- to allow for performing computations whose results are normally in Java only known at run–time, so these results can be used at compile–time when constructing TADDs;
- to interpret allocations that are normally absent in the TADD formalism.

In this paper we assume that direct or indirect recursion is not allowed in the considered fragment of the Java language. The following operations are allowed in the interpreted thread: allocations of new objects, including static initialisation, creation and starting of new threads, assignments and method calls,
arithmetic expressions that can be statically determined.

To implement the translation shown in [15], we first translate a Java code to an internal assembly language (see the next section). Than, the resulting assembly code is translated to a network of TADDs; this is presented in Section 4.

3. Internal Assembly Language (IAL)

This section describes an assembly language that is used internally by our J2TADD translator; in Section 3.6 we provide a formal grammar of a single operation of the language.

We start with an explanation of what we mean by dereferences and literals that we use in our internal assembly language. Then, we describe how the Java static and non–static methods are handled. Next, we discuss polymorphism and annotations. Finally, we define allowed operations and show their connections with object allocation, method calls, synchronisation and standard programming language constructs like assignment, expressions or loops.

3.1. Dereferences and literals

A dereference is a pair \(\text{object, field}\) of two variables, where the first one must be an object reference, including the null reference. Both \text{object} and \text{field} variables are always either local or static variables, and the variable \text{object} can be \text{null}. If the variable \text{object} is not \text{null}, then this is a dereference of a respective non–static field \text{field} of the Java object \text{object}, and this is the only case when there is performed an actual variable dereference. Textual representation of such a dereference is \text{object::field}. If the variable \text{object} is \text{null}, then the dereference refers to a local variable or a static field. Textual representation of such a reference is \text{field}. The type of a dereference in each case is the same as that of \text{field}.

Literals are constant values and can have one of the following types: \text{void}, \text{char}, \text{byte}, \text{int}, \text{short}, \text{long}, \text{float}, \text{double} and a Java type; TADDs currently support only integer types, though.

In our assembly language we refer to dereferences and literals as to values, i.e., we have \(\langle\text{value}\rangle ::= \langle\text{literal}\rangle | \langle\text{dereference}\rangle\).

3.2. Methods

There is no difference between static and non–static Java methods in our assembly language apart from one: a non–static method has an additional argument called \text{this}, which is of the type of the class containing the method. The argument references an instance of the object “this”, that defines the non–static context of the method. A constructor is simply a non–static method, but it is named after the class it constructs.

3.3. Instantiation and polymorphism

A run-time Java object gets its non–static fields initialised in its method \text{*object}. A class we treat as a special object. The static fields of a class are initialised in the method \text{*static}.

In our assembly language we define only one polymorphism rule. Namely, if the class B extends class A, then an object of the class B can be used when an object of the class A is required. A run-time object class type, as opposed to a variable compiled type, decides about which non–static method is
called. The other possible polymorphism rules can be enforced during parsing, semantic check or code generation, the assembly language itself does not define or enforce them.

3.4. Annotations

Annotations provide data about a program that is not part of the program itself. They have no direct effect on the operation of the code they annotate. Annotations have a number of uses, among them: information for the compiler, compiler-time and deployment-time processing, or run-time processing (some annotations are available to be examined at run-time).

In our assembly language methods can have annotations like in Java. There are also internal special annotations that begin with `@@` and annotate special methods:

- `@MARK` – do not call, the method is just a marker; it is replaced by a no operation, see the description of no operation for details;
- `@START_THREAD` – start a thread;
- `@IGNORE` – ignore any call of the method; it works like `@MARK` but it is included for clarity.

Back-ends and interpreters (see Section 4) can define their own annotations, and use them along with these two described in this section. For example, the interpreter used by the translator uses an annotation `@START_THREAD` to start a new thread, and the TADD backend uses an annotation `@RANDOM` to mark a method that generates random numbers. Both annotations are used together with the annotation `@MARK`. Normally, the user of the translator does not need to know or use these special annotations. Instead, there is a special library of classes like `Object` or `Thread`, in which certain methods are marked with these annotations. For example, `Thread.start()` is annotated with `@START_THREAD` in the library.

3.5. Operations

The following operations are defined in the internal assembly language:

- Allocation: `<dereference> = new <constructor> (<argument>*)`. It instantiates an object, calls the object’s method *object and then the respective constructor. Example:

  ```
  #c3 = new ->Sender(mch.abp.v1.Sender, mch.abp.v1.LossyChannel) channel
  ```

  An object `Sender` is created using a constructor that takes an argument of the type `mch.abp.v1.LossyChannel`. The argument is the copy of a local variable `channel` of the caller method. The other argument is the new object and provides the “this” local variable for the non-static context. It is, thus, of the type of the created object.

- An assignment: `<dereference> = <value>`. Example: `this::ackBit = ackBit`. This operation copies the value of the local variable `ackBit` to the field of the object of a non-static method.

- A binary expression: `<dereference> = <value> <operator> <value>`. It allows for standard Java binary operators and also for a concatenation of a string to any other value. Here the standard Java rules apply to determine the type of the reference variable; for example, an integer and a float produce a float. Only the following binary operators are currently supported by the TADDs, though: plus, minus, multiply, divide, modulus. Example: `#c5 = ignoreBit > 2`. Test if a local variable `ignoreBit` is greater than a constant 2, and store the result in a local Boolean variable `#c5`.  

• A conditional branch: `<value> ? <label> : <label>`. This condition value can be a dereference or a constant. The labels point to operations to jump to if the condition is, respectively, true or false. A label can be null which means that there is no respective jump. Example: `#c5?<null>:10`. If `#c5` is true, then go to the next operation. Otherwise, jump to the operation of index 10.

• A call to a method: `<dereference> = <method> (<argument>*)`. If the method returns void, the dereference is null. As in Java, the non–static methods are virtual. Argument "this" of non–static methods is always the first one. Example:

\[
\text{ignoreBit} = \rightarrow\text{nextInt}(:\text{java.util.Random}, \text{int})
\]

\[
\@\text{MARKER } \@\text{RANDOM } (\text{this})\text{this::random 5.}
\]

Call to a non–static method `nextInt(int)` of the object referred by a field `random` belonging to `int` turn to the object of the calling non–static method. The additional argument of the type `java.util.Random` passes a reference to “this” of the called method. A value of 5 is copied to the other parameter. The called method has two internal annotations: `@MARKER` and `@RANDOM`.

• An unconditional jump: `goto <label>`. Example: `goto 4`. Go to the operation at index 4.

• No operation: `no op [<dereference> = ] <annotation>* (<argument>*)`. It can be used as a holder of a label or of an annotation. Also, despite its name, it can perform something if the annotation specifies some special operation, that can not be expressed by the regular assembly language operations, but that is needed for some particular translation. In that case, the operation may optionally have arguments, and a dereference to hold the result of the operation. Any call to a method annotated with `@MARKER` is replaced by no operation. Example:

\[
\text{no op } @\text{MARKER } @\text{SLEEP } (i#0mch.abp.v2.Sender_run__#c8).
\]

Perform the special operation “sleep” on a thread referenced by `i#0mch.abp.v2.Sender_run__#c8`. The operation does not return anything, so the part `[<dereference> = ]` is absent.

• `return` – returns from a method.

• `sync begin|end <dereference>` – either the begin or the end of a synchronised code. The dereference references the semaphore. Example: the code of a synchronised method always starts with `sync <begin>` this and there is `sync <end>` this before each `return` operator in the method.

• An unary expression: `<dereference> = <operator> <value>`. It allows for standard Java unary operators. Only the following unary operators are currently supported by the TADDs, though: arithmetic negation and Boolean negation. Example: `#c7::ackBit = !#c7::ackBit`. Negate the field ackBit of the object referred by the variable `#c7`.

We further assume that:

• A return value of a method is in a local variable named `*retval`.

• A label is simply an index of the target operation, or, in the case of a conditional branch, optional null meaning no jump, but execution of the following operation instead.

• A signature, like in Java, is an unique key that defines a method. A signature contains the method’s name and types of arguments. It does not contain the type of “this” argument of non–static methods.
3.6. Grammar of an operation

This section defines a formal grammar of our assembly language operation \( \langle \text{operation} \rangle \), provided in the BNF notation.

- \( \langle \text{allocation} \rangle ::= \langle \text{dereference} \rangle = \text{new} \langle \text{method call} \rangle (\langle \text{argument} \rangle \ast) \)
- \( \langle \text{annotation} \rangle ::= \langle \text{identifier} \rangle \)
- \( \langle \text{assignment} \rangle ::= \langle \text{dereference} \rangle = \langle \text{value} \rangle \)
- \( \langle \text{binary_expression} \rangle ::= \langle \text{dereference} \rangle = \langle \text{value} \rangle \langle \text{binary_operator} \rangle \langle \text{value} \rangle \)
- \( \langle \text{binary_operator} \rangle ::= \text{PLUS} | \text{MINUS} | \text{MULTIPLY} | \text{DIVIDE} | \text{MODULUS} | \text{INCLUSIVE}_\text{OR} | \text{EXCLUSIVE}_\text{OR} | \text{AND} | \text{EQUAL} | \text{CONDITIONAL}_\text{OR} | \text{CONDITIONAL}_\text{AND} | \text{INEQUAL} | \text{LESS} | \text{GREATER} | \text{LESS}_\text{OR}_\text{EQUAL} | \text{GREATER}_\text{OR}_\text{EQUAL} | \text{SHIFT}_\text{LEFT} | \text{SHIFT}_\text{RIGHT} | \text{SIGNED}_\text{SHIFT}_\text{RIGHT} \)
- \( \langle \text{call} \rangle ::= \langle \text{dereference} \rangle = \langle \text{method call} \rangle (\langle \text{argument} \rangle \ast) \)
- \( \langle \text{expression} \rangle ::= \langle \text{unary_expression} \rangle | \langle \text{binary_expression} \rangle \)
- \( \langle \text{identifier} \rangle \) is a string of characters.
- \( \langle \text{jump} \rangle ::= \langle \text{unconditional_jump} \rangle | \langle \text{conditional_branch} \rangle \)
- \( \langle \text{label} \rangle \) is an index of the target operation. Indices start at 0.
- \( \langle \text{literal} \rangle \) is a constant.
- \( \langle \text{method_call} \rangle ::= \rightarrow \langle \text{method_signature} \rangle (\langle \text{dereference} \rangle \ast) (\langle \text{annotation} \rangle \ast) \)
- \( \langle \text{method_signature} \rangle \) is the signature of a method, see Sec.3.5 for details.
- \( \langle \text{no_op} \rangle ::= \text{no_op}\{\langle \text{dereference} \rangle = \} (\langle \text{annotation} \rangle \ast) \)
- \( \langle \text{operation} \rangle ::= \langle \text{allocation} \rangle | \langle \text{assignment} \rangle | \langle \text{expression} \rangle | \langle \text{jump} \rangle | \langle \text{method_call} \rangle | \langle \text{return} \rangle | \langle \text{sync} \rangle \)
- \( \langle \text{dereference} \rangle ::= [\langle \text{variable_name} \rangle : : ]\langle \text{variable_name} \rangle \)
- \( \langle \text{return} \rangle ::= \text{return} \)
- \( \langle \text{sync} \rangle ::= \text{sync begin}|\text{end} \langle \text{dereference} \rangle \)
- \( \langle \text{unary_expression} \rangle ::= \langle \text{dereference} \rangle = \langle \text{unary_operator} \rangle \langle \text{value} \rangle \)
- \( \langle \text{unary_operator} \rangle ::= \text{NEGATION} | \text{CONDITIONAL}_\text{NEGATION} | \text{BITWISE}_\text{COMPLEMENT} \)
- \( \langle \text{unconditional_jump} \rangle ::= \text{goto} \langle \text{label} \rangle \)
- \( \langle \text{value} \rangle ::= [\langle \text{literal} \rangle ]\langle \text{dereference} \rangle \)
- \( \langle \text{variable_name} \rangle ::= \langle \text{identifier} \rangle \)

4. Translation from IAL to TADDs

Once the assembly code is generated, two stages remain to convert the code to TADDs: interpreting the main thread and then generating transitions from the other threads.

4.1. Interpreter

The role of the interpreter is to initialise variables, load needed classes, create objects and threads.

**Threads.** As in Java, the start method of the main thread is the main method of the translated application. Only a single main method within the application is allowed by the translator.

The interpreter is started with this single thread and ends once the main thread ends. All other threads, called here TADD threads, created by the interpreter when executing the main thread are translated to TADDs, one TADD per thread.
Library and annotations. The interpreter uses a TADD–specific library that defines the needed Java classes like Object or Thread. As already discussed, some methods in the library are marked with special annotations beginning with double ‘@’. Let us describe these annotations in detail.

The annotations @@IGNORE, @@MARKER and @@START_THREAD are used as defined by the assembly language, and were already described in Sec.3.4. Additional TADD–specific annotations defined in the library are as follows:

- @@NOTIFY – perform notify() on an object, used with marker methods, usually only with Thread.notify().
- @@RANDOM – generate a random number in the range 0 ... method’s integer argument - 1, used with marker methods, usually only with Random.nextInt(int).
- @@SLEEP – sleep by a given number of milliseconds, used with marker methods, usually only with Thread.sleep(int).
- @@WAIT – perform wait() on an object, used with marker methods, usually only with Thread.wait().

4.2. Building TADDs

This section describes the process of generating TADDs after interpreting the main thread.

Virtual call resolution. The first step is a virtual call resolution. In Java, a called non–static method is determined not by compile–time type of an object, but by the run-time type of an object. After the interpreter is already run and run-time types are known, some calls can be resolved. If it is not possible because of a null variable or a variable not initialised at the interpreter stage, an error is reported.

Inlining calls. Once the calls are resolved, it is possible to ‘flatten’ the code by the translator. To do so, the code is recursively inlined with a thread, beginning at each thread’s method run(), so that, in effect, there should be no calls left in the ‘flattened’ code. The operation is performed because no stack is available in the generated TADDs for calling methods, and also because inlined code allows for more efficient optimisations like reduction of variables and assignments.

Optimisation. The next step is an optimisation of the code like: value propagation, optimising branches with static conditions, compacting jump and assignment chains, checking for dead code, removal of assignments and locals that are unused. This step is performed even if the code had already been optimised before the discussed inlining of calls. It is because the inlining may allow for substantial further optimisations, as mentioned above.

Ambiguity check. The J2TADD translator allows for assignments of reference variables within the TADD threads, if the translator can determine that these assignments do not cause a reference to be ambiguous at compile time. The translator checks only the local reference variables for ambiguity, and thus, assignments to non–local reference variables within TADD threads are forbidden. Ambiguity means here that the dereferenced variable can have more than a single value at the time of being dereferenced, or at least that it was not possible to determine statically, by tracing execution paths within a method, that there is only one such value possible. Such an ambiguity of a reference variable would make it impossible to statically dereference the variable at compile time. This in turn would require the actual dereferences to be performed at run–time. TADDs, though, do not support dereferences and a run-time emulation of dereferences is not implemented.

Generating transitions. Most assembly language operations are straightforwardly translated to TADDs: assignments, jumps, binary and unary expressions and conditional branches. However, there are some
sequences of assembly language operations that are treated as a whole and as such transformed to respective TADD transitions:

- a conditional expression followed by a conditional branch, which uses the result of the conditional expression, is transformed to two opposite conditional expressions on two transitions. Example:

  \[
  0 \ c = a < 1 \\
  1 \ c \ ? \ <null> : 3 
  \]

  is translated to two conditional expressions: \( a < 1 \) on a transition from state 1 to state 2 and \( a \geq 1 \) on a transition from state 1 to state 3.

- a marker method @RANDOM followed by a marker method @SLEEP that uses the result of the first marker method is transformed to a respective clock condition. Example:

  \[
  0 \ c = @@MARKER \@RANDOM \ (\text{random} \ 150) \\
  1 \ \text{no op} \ @@MARKER \@SLEEP \ (c) 
  \]

  is translated to a clock condition \((x \geq 0) \land (x < 150)\).

Annotations @NOTIFY, @WAIT and synchronisation operations cause a new semaphore TADD to be created for the respective synchronisation object if the semaphore does not exist yet. Transitions are added to the semaphore as necessary. As required by Java, within a single thread, synchronisation operations on run-time object \( k \) are ignored if they are nested within synchronisation operations that are also on the object \( k \).

5. Example translation

Let us discuss an example translation of a Java program, which implements a possible solution of the well known dining philosophers problem (for the discussion of the problem see 6.2), to our assembly language (IAL), and then to TADDs. The translated class (program) College3 extends Thread and objects of the class represents threads in the output network of automata. The source of the class is given in Fig. 1. IAL representation of one of the actual threads is shown in Fig. 2. The IAL code is eventually translated into one of the automata. The transitions of the automaton, written in the \\textit{Verica} format, are shown in Fig. 3; the notation transition \( x \ y \ z \) means that there is a transition from location \( x \) to location \( y \) labelled by \( z \). Because of the length of the representation, comments and lines containing only the keywords \texttt{reset} and \texttt{end}, were removed from the file.

Lines in each of the three representations are prefixed with \( s \) for the source file, \( i \) for the IAL file and \( t \) for the output file, as seen in the figures, and so the lines will further be referred to in this section. Counterparts of each line in the different representations are shown in Table 1. If a method is called twice, its code is translated twice. In the table, a group of lines related to such a single call is separated from possible other groups with a semicolon.

Let us now discuss the example translation in detail.

The thread begins with calling \texttt{run()}, that begins in \( s61 \). The method is non–static, and thus, its local variable \texttt{this} has to be set to point to the method’s object. As can be seen from the mapping, there is a related assignment operation in \( i0 \). The method itself begins in \( s62 \) with a condition that can be evaluated at compile time. Such operations, and several others within the thread, do not have any
public class College3 {
    public static void main(String args []) {
        Fork fork0 = new Fork(false);
        Fork fork1 = new Fork(false);
        Fork fork2 = new Fork(false);

        Philosopher p0 = new Philosopher(0, fork0, fork1);
        Philosopher p1 = new Philosopher(1, fork1, fork2);
        Philosopher p2 = new Philosopher(2, fork2, fork0);

        (new Thread(p0)).start();
        (new Thread(p1)).start();
        (new Thread(p2)).start();
    }
}

class Fork {
    private boolean unavailable;

    public Fork(boolean unavailable) {
        this.unavailable = unavailable;
    }

    public synchronized void acquire() {
        while (unavailable) {
            try {
                wait();
            } catch (InterruptedException e) {
            }
        }
        unavailable = true;
        notify();
    }

    public synchronized void release() {
        unavailable = false;
        notify();
    }
}

class Philosopher implements Runnable {
    private int nr;
    private Fork left, right;

    public Philosopher(int nr, Fork left, Fork right) {
        this.nr = nr;
        this.left = left;
        this.right = right;
    }

    private void eating() {
        try {
            Thread.sleep(1);
        } catch (InterruptedException e) {
        }
    }

    public void run() {
        while (true) {
            left.acquire();
            right.acquire();
            eating();
            right.release();
            left.release();
        }
    }
}

Figure 1. Java source code of the program College3.

counterparts in both IAL and the TADDS formalism, as there is no need to execute anything related to operations like that at run time.

The first operation that is actually translated to a network of TADDS is in s64. As it is a call to a marker method, its counterpart in IAL is an annotated no-operation. The annotations @MARKER @SLEEP and the argument 5 describes the original operation. The no-operation is in turn translated to transitions t1, t2, t10 and t11 that contain a reset of the clock x1 and a condition x1 >= 5. The catch block at s65 and s66 is empty. While the translator does not support exceptions, it accepts exception–related constructs that do not effectively contain any exception–related code, assuming, though, that no exceptions will actually be thrown.

The next operation is the call to a non–static synchronized method acquire() in s69. In IAL, translation of the call begins with the assignment of the local variable this, like it has already been
discussed. Then, as the call is not a marker method like Thread.sleep, but a method with an actual code, inlining of the code begins. A code in a synchronized method, if the call operation is not already synchronized with a given lock, needs to be preceded with a synchronization begin operation, in this case in i3, and a matching synchronization end operation, in the case in i8. As shown in the mapping, there are equivalent automata transitions in t13 and t21. There are also lines i10, i15, t22 and t31 shown in the array, yet after semicolons. They contain the synchronization code as well, but related to another call of the method acquire().

The condition in s27 is translated to i4 and then to two transitions with conditions in t14, t15, t16 and t17. The two conditions are opposite and, as it can be seen in the automata code, split the thread into two branches.

Within the inlined method, there is a call to a marker method in s29, translated, like the previous call to a marked method, into an annotated no-operation, but the annotations are different this time – @MARKER @WAIT, and there is no argument. The operation is translated to a set of automata transitions,
t1 transition 0 6 24
t2 reset x1
t3 transition 1 19 25
t4 x1 >= 1
t5 reset x1
t6 transition 2 13 8
t7 transition 3 2 26
t8 transition 4 8 0
t9 transition 5 4 27
t10 transition 6 7 28
t11 x1 >= 5
t12 reset x1
t13 transition 7 8 2
t14 transition 8 9 29
t15 cond z0 = c1
t16 transition 8 10 30
t17 cond z0 = c0
t18 transition 9 5 6
t19 transition 10 11 31
t20 assign c1 + c0 to z0
t21 transition 11 12 4
t22 transition 12 13 10
t23 transition 13 14 32
t24 cond z1 = c1
t25 transition 14 15 33
t26 cond z1 = c0
t27 transition 14 3 14
t28 transition 15 16 34
t29 assign c1 + c0 to z1
t30 transition 17 18 35
t31 transition 16 17 12
t32 transition 19 20 10
t33 transition 18 1 36
t34 reset x1
t35 transition 21 22 37
t36 assign z4 - c1 to z4
t37 transition 21 22 38
t38 cond z4 = c0
t39 transition 20 21 39
t40 assign c0 + c0 to z1
t41 transition 23 24 2
t42 transition 22 23 12
t43 transition 25 26 40
t44 assign z3 - c1 to z3
t45 transition 25 26 41
t46 cond z3 = c0
t47 transition 24 25 42
t48 assign c0 + c0 to z0
t49 transition 26 0 4

Figure 3. Shortened T ADDs representation of one of the threads of the objects College3.

as described in the mapping. The assignment operation in s37 has equivalent assignments in i20, t39 and t40.

The next call in run(), in the line s69, differs from the previous one in a different value assigned to the called method’s this, and in being tagged with @observable. The two differences are seen in the lines i9 – i15, when compared to the already discussed i2 – i8. The IAL operations are tagged this time, and in the T ADDs formalism, a different variable is assigned – compare t39, t40 with t47, t48. This is because the field unavailable belongs to two different objects in each of the two calls in, respectively, s67 and s69.

Even that all operations within i9 – i15 are tagged, the TADD backend, while generating transitions from these operations, will check for the filter .annotation NOTIFY_THREAD and will mark only these transitions as observable that are annotated with @NOTIFY_THREAD.

The next operation in the method run() is a call to a not synchronized method eating(). The method is tagged with generateHead, what is reflected by an additional transition in t30.

The next two calls are to release(). It is a non–static synchronized method, as acquire(). The method contains a call to a marker method notify(). A counterpart IAL code is seen for example in i21. The code of this particular IAL operation is then translated to transitions shown in lines t35, t36, t37, t38.

The last operation in run() is a jump in s73 to the condition of the while loop. The jump is translated to a goto operation in i28, which in turn is translated to a transition in t49. As seen in the mapping, to this transition has also been translated the synchronization end operation at i27.

6. Case study

The main aim of this paper was to show a new model checking method for verification of concurrent programs written in Java. This method consists in translating a Java program to a network of TADDs, and then applying the existing model checking tools accepting a description of a network of TADDs as input. As we have already mentioned at the very beginning of the paper, the model checking tools like
Table 1. Mutual dependencies between the lines in each representation.

<table>
<thead>
<tr>
<th>Java</th>
<th>IL</th>
<th>Verics</th>
</tr>
</thead>
<tbody>
<tr>
<td>s26</td>
<td>i3; i10</td>
<td>t13; t22</td>
</tr>
<tr>
<td>s27</td>
<td>i4; i11</td>
<td>t14, t15, t16, t17; t23, t24, t25, t26</td>
</tr>
<tr>
<td>s29</td>
<td>i5; i12</td>
<td>t18, t9; t27, t7</td>
</tr>
<tr>
<td>s32</td>
<td>i6; i13</td>
<td>t8; t6</td>
</tr>
<tr>
<td>s33</td>
<td>i7; i14</td>
<td>t19, t20; t28, t29</td>
</tr>
<tr>
<td>s34</td>
<td>i8; i15</td>
<td>t21; t31</td>
</tr>
<tr>
<td>s36</td>
<td>i19; i24</td>
<td>t32; t41</td>
</tr>
<tr>
<td>s37</td>
<td>i20; i25</td>
<td>t39, t40; t47, t48</td>
</tr>
<tr>
<td>s38</td>
<td>i21; i26</td>
<td>t35, t36, t37, t38; t43, t44, t45, t46</td>
</tr>
<tr>
<td>s39</td>
<td>i22; i27</td>
<td>t42; t49</td>
</tr>
<tr>
<td>s53</td>
<td>i16</td>
<td>t30</td>
</tr>
<tr>
<td>s56</td>
<td>i17</td>
<td>t33, t34, t3, t4, t5</td>
</tr>
<tr>
<td>s61</td>
<td>i0</td>
<td>-</td>
</tr>
<tr>
<td>s64</td>
<td>i1</td>
<td>t1, t2, t10, t11, t12</td>
</tr>
<tr>
<td>s67</td>
<td>i2</td>
<td>-</td>
</tr>
<tr>
<td>s68, s69</td>
<td>i9</td>
<td>-</td>
</tr>
<tr>
<td>s71</td>
<td>i18</td>
<td>-</td>
</tr>
<tr>
<td>s72</td>
<td>i23</td>
<td>-</td>
</tr>
<tr>
<td>s73</td>
<td>i28</td>
<td>t49</td>
</tr>
</tbody>
</table>

Uppaal or VerICS accept networks of TADDs as input. Thus, to verify concurrent programs written in Java using those tools, we had to develop the J2TADD tool that translates a Java program to a network of TADDs.

In this section, we report on results that we have got when evaluating the effectiveness of the proposed verification method by means of four well known concurrency examples written in Java. We do this by comparing our results with those returned by the tools JPF and Bandera.

For all the considered examples we have searched for deadlock states, and additionally, we have tested a race condition by using JPF, Uppaal, and the BMC4TADD module of VerICS. A state is a deadlock state if there are no outgoing action transitions neither from the state itself or any of its delay successors.

In order to search for a deadlock state JPF tests for every non-end state if there is any runnable thread left. In Uppaal, in order to search for a deadlock state, a special state formula $E\Box\text{deadlock}$ that is satisfied for all deadlock states is used. It is worth to mention that using Uppaal one can also handle the following temporal properties: possibility, invariants, potentially always, eventually, if a request occurs then it will be eventually acknowledge.

In BMC4TADD to search for a deadlock state we test reachability of a state satisfying certain (usually
undesired) property. For this the transition relation of a given network of TADDs is unfolded up to some depth \( k \), and encoded as a propositional formula. Then, the reachability property to be tested is encoded as a propositional formula as well, and satisfiability of the conjunction of these two formulae is checked using a SAT–solver. If the conjunction is satisfiable, one can conclude that a counterexample (a path to an undesirable state) was found. Otherwise, the value of \( k \) is incremented. The above process can be terminated when the value of \( k \) is equal to the diameter of the system, i.e., to the maximal length of a shortest path between its two arbitrary states.

All of the experiments have been performed on a computer equipped with the processor Intel Core 2 Duo (2 GHz), 2 GB main memory and the operating system Linux. Moreover, we have set the time–out limit to 15min for RSAT–solver to get the answer.

### 6.1. Race condition

**Problem description.** In practical multithreaded applications, it is common that two or more threads need to share access to the same objects. However, if two threads have access to the same object and each calls a method that modifies the state of the object at the same time, then the result can be partly what one thread wrote and partly what the other thread wrote. Depending on the order in which the object was accessed, a corrupted object can result. Such a situation is called a race condition.

**An example of a race condition.** In our example program `RaceCondition4.java` (see Listing 1) there are two threads that run concurrently and access to a shared variable that is initially set to 0. Each thread gets a value of the shared variable, increase this value by one and write back the updated value to the variable. The above operation is repeated \( n \) times by both threads. Therefore, one could expect that the final value of the shared variable will be equal to \( 2n \). However, one can observe that there exit executions of the program which end with the value of the shared variable that is less than \( 2n \). This is because these threads do not lock the shared variable while it is being accessed. In a proper realisation these threads should lock the shared variable while it is being accessed and then should unlock it when they are finished.

We are able to detect the above race condition by translation of the Java code of our example program to a network of TADDs and then checking reachability of a state in which the final value of the shared variable is less than \( 2n \); the reachability checking was done by the tools Uppaal and BMC4TADD. W have also tested the race condition property by means of the the JPF tool; the Bandera is not able to check this property. The results for this property are in Table 2.

<table>
<thead>
<tr>
<th>Tools</th>
<th>( n )</th>
<th>sec.</th>
<th>MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2TADD + BMC4TADD</td>
<td>4</td>
<td>2797.2</td>
<td>103.6</td>
</tr>
<tr>
<td>J2TADD + BMC4TADD</td>
<td>5</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>JPF</td>
<td>( \leq 16000 )</td>
<td>( \leq 57.79 )</td>
<td>( \leq 471 )</td>
</tr>
<tr>
<td>J2TADD + Uppaal</td>
<td>( \leq 16000 )</td>
<td>( \leq 2.9 )</td>
<td>( \leq 80.4 )</td>
</tr>
</tbody>
</table>

Table 2: Race condition

### 6.2. Dining Philosophers Problem

**Protocol Description.** The description of the dining philosophers problem (DPP) we provide below is based on that in [7]. Consider \( n \) (\( n \geq 2 \)) philosophers. Each philosopher has a room in which he engages in his professional activity of thinking. There is also a common dining room, furnished with a circular
public class RaceCondition4 {
    public static void main (String[] args) {
        Variable variable = new Variable();
        Thread b1 = new Thread (new Beaver (variable, 4));
        Thread b2 = new Thread (new Beaver (variable, 4));
        b1.start(); b2.start();
        (new Thread (new Observer (b1, b2, variable, 8))).start();
    }
}

class Variable {
    private int value;
    Variable() { this.value = 0; }
    public synchronized int getValue() { return value; }
    public synchronized void setValue (int value) { this.value = value; }
}

class Observer implements Runnable {
    private Thread b1, b2;
    private Variable variable;
    private int expResult;
    private boolean error;
    public Observer (Thread b1, Thread b2, Variable variable, int expResult) {
        this.b1 = b1; this.b2 = b2;
        this.variable = variable;
        this.expResult = expResult;
        this.error = false;
    }
    public void run() {
        try { b1.join(); } catch (Exception e) {}
        try { b2.join(); } catch (Exception e) {}
        if (variable.getValue() != expResult) {
            /* @observable */
            error = true;
        }
    }
}

class Beaver implements Runnable {
    private Variable variable;
    private int limit;
    public Beaver (Variable variable, int limit) {
        this.variable = variable;
        this.limit = limit;
    }
    public void run() {
        for (int j = 0; j < limit; ++j) {
            int tmp = variable.getValue() + 1;
            variable.setValue (tmp);
        }
    }
}

Listing 1. Java source code of the race condition problem
table, surrounded by \( n \) chairs, each labelled by the name of the philosopher who is to sit in it. On the left of each philosopher there is a fork, and in the centre stands a large bowl of spaghetti, which is constantly replenished. Whenever a philosopher eats he has to use both forks, the one on the left and the other on the right of his plate. A philosopher is expected to spend most of his time thinking, but when he feels hungry, he goes to the dining room, sits down on his own chair, and picks up the fork on his left provided it is not used by the other philosopher. If the other philosopher uses it, he just has to wait until the fork is available. Then the philosopher tries pick up the fork on his right. When a philosopher has finished he puts down both his forks, exits dining-room and continues thinking.

We have implemented a possible solution of the DPP problem that could lead to a deadlock; one can get the implementation by removing from Listing 2 the class \textit{Lackey} and all the occurrences of instructions containing the variable \( s \). The deadlock can happen, if every philosopher sits down on his own chair at the same time and picks up his left fork. Then all forks are locked and none of the philosophers can successfully pick up his right fork. As a result, every philosopher waits for his right fork that is currently being locked by his right neighbour, and hence a deadlock occurs. The results for the deadlock property are in Table 3.

Assume now another solution for DPP (see Listing 2), where there is a lackey who ensures that at most \( n - 1 \) philosophers can be present in the dining room at the same time. This lackey ensures that no deadlock is possible (see Table 4 for the results).

<table>
<thead>
<tr>
<th>Tools</th>
<th>No. Ph</th>
<th>sec.</th>
<th>MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2TADD + BMC4TADD</td>
<td>5</td>
<td>12 217</td>
<td>279.2</td>
</tr>
<tr>
<td>JPF</td>
<td>4</td>
<td>2.21</td>
<td>3.7</td>
</tr>
<tr>
<td>JPF</td>
<td>5</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>J2TADD + Uppaal</td>
<td>60</td>
<td>1.16</td>
<td>41.9</td>
</tr>
<tr>
<td>Bandera</td>
<td>60</td>
<td>117.02</td>
<td>3.3</td>
</tr>
</tbody>
</table>

Table 3: Dining Philosophers. Deadlock.

<table>
<thead>
<tr>
<th>Tools</th>
<th>No. Ph</th>
<th>sec.</th>
<th>MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2TADD + Uppaal</td>
<td>5</td>
<td>52.22</td>
<td>418.7</td>
</tr>
<tr>
<td>J2TADD + Uppaal</td>
<td>6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>JPF</td>
<td>4</td>
<td>16.47</td>
<td>3.7</td>
</tr>
<tr>
<td>JPF</td>
<td>5</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Bandera</td>
<td>2</td>
<td>76.31</td>
<td>4.6</td>
</tr>
<tr>
<td>Bandera</td>
<td>3</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

Table 4: Dining Philosophers. Absence of deadlocks.

### 6.3. Single Sleeping Barber Problem

Consider a hypothetical barber shop that has one barber, one barber chair, and a waiting room with several chairs for customers. When a barber finishes cutting a customer’s hair, he fetches another customer from the waiting room if there is a customer, or the barber sits in his chair and sleeps if there are no customers. A customer who needs a haircut enters the waiting room. If the waiting room is full, the newly arrived
public class College5L {
    public static void main(String args []) {
        Fork f0 = new Fork(false); Fork f1 = new Fork(false);
        Fork f2 = new Fork(false); Fork f3 = new Fork(false);
        Fork f4 = new Fork(false);
        Lackey s = new Lackey(4);
        Phil p0 = new Phil(0,f0,f1,s); Phil p1 = new Phil(1,f1,f2,s);
        Phil p2 = new Phil(2,f2,f3,s); Phil p3 = new Phil(3,f3,f4,s);
        Phil p4 = new Phil(4,f4,f0,s);
        (new Thread(p0)).start(); (new Thread(p1)).start();
        (new Thread(p2)).start(); (new Thread(p3)).start();
        (new Thread(p4)).start();
    }
}

class Fork {
    private boolean unavailable;
    public Fork(boolean unavailable) { this.unavailable = unavailable; }
    public synchronized void acquire() {
        while (unavailable) { try {wait();} catch (Exception e){} }
        unavailable = true;
    }
    public synchronized void release() { unavailable = false; notify(); }
}

class Lackey {
    private int m; private int max;
    public Lackey(int max) {this.max = max;}
    public synchronized void acquire() {
        while (m >= max) { try {wait();} catch (Exception e) {} }
        ++m;
    }
    public synchronized void release() { --m; notify(); }
}

class Phil implements Runnable {
    private int nr; private Lackey s;
    private Fork left, right;
    public Phil(int nr, Fork left, Fork right, Lackey s) {
        this.nr = nr; this.s = s;
        this.left = left; this.right = right;
    }
    public void run() {
        while (true) {
            s.acquire(); left.acquire();
            // @{( inline infinite }, . annotation @@NOTIFY_THREAD) observable
            right.acquire(); right.release();
            left.release(); s.release();
        }
    }
}
customer simply leaves. If the barber is busy but in the waiting room there is a vacant chair available, the customer takes a seat. If the waiting room is empty and the barber is sleeping, the customer sits in the barber chair and wakes the barber up.

Implementing a wrong solution of the problem can lead either to deadlock. The deadlock can happen, if the barber waits on a customer and a customer waits on the barber. We have taken a proper implementation of the problem from the Bandera website; it is based on a solution discussed in book [13] and we have checked the solution for absence of deadlocks and the results are in Table 5.

<table>
<thead>
<tr>
<th>Tools</th>
<th>No. Customers</th>
<th>sec.</th>
<th>MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2TADD+Uppaal</td>
<td>4</td>
<td>107</td>
<td>42.7</td>
</tr>
<tr>
<td>JPF</td>
<td>3</td>
<td>18.13</td>
<td>3.7</td>
</tr>
<tr>
<td>JPF</td>
<td>4</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Bandera</td>
<td>2</td>
<td>612</td>
<td>3.3</td>
</tr>
<tr>
<td>Bandera</td>
<td>3</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

Table 5: Single Sleeping Barber. Absence of deadlocks.

### 6.4. Readers and writers

The readers and writers problem has two types of threads accessing the shared data. The first type, called readers, only wants to read the shared data. The second type, called writers, may want to modify the shared data. If a writer is accessing the shared data, then no other writer or reader can do this.

We have implemented a possible solution of the above problem for \( n \) writers and \( n \) readers, \( n \leq 1 \) (see Listing 3). These programs consist of at least 2 threads: one reader and one writer. To increase the size of the problem, additional readers and writers can be added. This solution does not admit deadlocks; the results are in Table 6.

<table>
<thead>
<tr>
<th>Tools</th>
<th>No. of Readers and Writers</th>
<th>sec.</th>
<th>MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>J2TADD+Uppaal</td>
<td>2</td>
<td>0.02</td>
<td>1.5</td>
</tr>
<tr>
<td>J2TADD+Uppaal</td>
<td>5</td>
<td>36.9</td>
<td>138.4</td>
</tr>
<tr>
<td>J2TADD+Uppaal</td>
<td>6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>JPF</td>
<td>2</td>
<td>3.59</td>
<td>4.9</td>
</tr>
<tr>
<td>JPF</td>
<td>3</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>Bandera</td>
<td>2</td>
<td>150.3</td>
<td>115.54</td>
</tr>
<tr>
<td>Bandera</td>
<td>3</td>
<td>-</td>
<td>-</td>
</tr>
</tbody>
</table>

Table 6: Readers and writers. Absence of deadlocks.

### 7. Summary

The J2TADD translator is not a self-contained verification system, but instead it provides an output for a verification system like Uppaal or VerICS, and it can be used as a part of a modular chain of such tools. Thus, users can reuse their knowledge of VerICS, Uppaal or other tools that as a input take a network of TADDs to use the translator to validate various concurrent programs written in Java. The translator
```java
public class R2W2 {
    public static void main(String args[]) {
        ReadingRoom readingRoom = new ReadingRoom();
        Reader r0 = new Reader(0, readingRoom);
        Reader r1 = new Reader(1, readingRoom);
        Writer w0 = new Writer(0, readingRoom);
        Writer w1 = new Writer(1, readingRoom);
        r0.start(); r1.start(); w0.start(); w1.start();
    }
}

class ReadingRoom {
    private int numberOfReaders = 0;
    public synchronized void startReading() {
        while (numberOfReaders < 0) { try { wait(); } catch (Exception e) {} }
        ++numberOfReaders;
    }
    public synchronized void stopReading() {
        --numberOfReaders;
        if (numberOfReaders == 0) { notify(); }
    }
    public synchronized void startWriting() {
        while (numberOfReaders != 0) { try { wait(); } catch (Exception e) {} }
        numberOfReaders = -1;
    }
    public synchronized void stopWriting() {
        numberOfReaders = 0; notify();
    }
}

class Reader extends Thread {
    private int nr;
    private ReadingRoom readingRoom;
    public Reader(int nr, ReadingRoom readingRoom) {
        this.nr = nr; this.readingRoom = readingRoom;
    }
    public void run() {
        while (true) { readingRoom.startReading(); readingRoom.stopReading(); }
    }
}

class Writer extends Thread {
    private int nr;
    private ReadingRoom readingRoom;
    public Writer(int nr, ReadingRoom readingRoom) {
        this.nr = nr; this.readingRoom = readingRoom;
    }
    // @generateHead */
    private void writing() {} 
    public void run() {
        while (true) {
            // @(.inline infinite, .annotation @NOTIFY,THREAD)observable */
            readingRoom.startWriting();
            // @observable */
            writing(); readingRoom.stopWriting();
        }
    }
}
```

Listing 3. Java source code of the readers and writers problem
performs a number of optimisations to decrease the often high memory and time requirements of model checking.

We have provided four examples of some well–known concurrency problems and the experiments confirm that our approach provides a valuable aid for Java software verification. In particular, we have shown that our translator together with the tool Uppaal or the SAT-based reachability module BMC4TADD of VerICS works as good as the Bandera of JPF tools (see 3) or it performs better (see Tables).

References